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				Application Number	10/717,955
				Filing Date	November 20, 2003
				First Named Inventor	Ha C. Vu
				Art Unit	HA 2816
				Examiner Name	Not Yet Assigned HAI L. NGUYEN
				Attorney Docket Number	08211/0200374-USO
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
HA	CA	Marie, Herve, et al. "R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display", IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1009-1013.	
HA	CB	Marie, Herve, et al. "Correction to R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display", IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, pp. 1253.	

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Examiner Signature	HAI L. NGUYEN	Date Considered	08/13/2004
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